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10/21/2003

RITTER, LANG & KAPLAN
12930 SARATOGA AE. SUITE D1
SARATOGA, CA 95070



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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/275,726	03/24/1999	BULENT DERVISOGLU	19705-000100	1134

TITLE OF INVENTION: ON-CHIP SERVICE PROCESSOR FOR TEST AND DEBUG OF INTEGRATED CIRCUITS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	YES	\$665	\$0	\$665	01/21/2004
EXAMINER	ART UNIT	CLASS-SUBCLASS			
TON, DAVID	2133	714-726000			

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2

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

On-Chip Technologies, Inc.

Los Altos Hills, California

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

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- ☐ Advance Order - # of Copies _____

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(Date)

Jeffrey W. Gluck, Reg. No. 44,457

11/19/03

11/21/2003 AAD0F02 0000063 09275726

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PTOL-85 (Rev. 10/03) Approved for use through 04/30/2004.

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